**DAILY ASSESSMENT REPORT**

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| **Date:** | **07 July 2020** | **Name:** | **Rubesh Murthy G** |
| **Course:** | **Technical support fundamentals** | **USN:** | **4AL18EC044** |
| **Topic:** | **Digital Logic and Computer architecture.** | **Semester & Section:** | **4th sem ‘A’ sec** |
| **GitHub Repository:** | **Rubesh-Pythonite** |  |  |

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| **SESSION DETAILS** |
| **Image of session** |
| **Report**  **Session was all about:**   * **Computer Language.** * **Character encoding.** * **Binary.** * **Supplemental reading on logic gates.** * **How to count in binary.** * **Practice Quiz – Binary.** * **Abstraction.** * **Computer architecture overview.** * **Kevin advice.** * **Practice quiz – Computer architecture.** |